

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,222,278 B2
APPLICATION NO. : 10/666770
DATED : May 22, 2007
INVENTOR(S) : Charles Moore

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 2 Line 4 Delete "fo" and insert -- for --, therefor.

Col. 8 Line 23 In Claim 1, delete "hyteresis" and insert -- hysteresis --, therefor.

Col. 8 Line 40 In Claim 5, after "hysteresis" delete "circuit".

Col. 8 Line 45(Approx.) In Claim 6, after "hysteresis" delete "circuit".

Col. 9 Line 38 In Claim 13, delete "BoundarScan" and insert -- Boundary-Scan --, therefor.

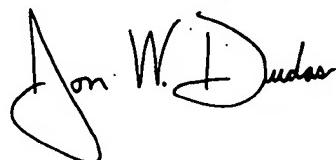
Col. 10 Line 34 In Claim 19, before "board" delete "a" and insert -- the --, therefor.

Col. 10 Line 36(Approx.) In Claim 20, delete "claim 12," and insert -- claim 19, --, therefor.

Col. 10 Line 37(Approx.) In Claim 20, before "board" delete "a" and insert -- the --, therefor.

Signed and Sealed this

Sixteenth Day of December, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office